

Time Table of Electrical Engg. Department w.e.f. 30/07/2026

Date: 23/07/2026

Notice No. 656

Day	Time	Period	3rd Sem	5th sem
Monday	9:00-10:00	1	EMI(RS)	IECD(VA)
	10:00-11:00	2	EEM (VA)	PS(RS)
	11:00-12:00	3	A&D.E(SK)	IECD LAB (VA) G1
	12:00-1:00	4	EM/C-I(SK)	PS LAB(RS)G2
	1:00-2:00	5	LUNCH	
	2:00-3:00	6	EM/C-I(SK)	MPW-G1(JS)
	3:00-4:00	7	EMI LAB -G1(RS)	MPW-G2(VA)
	4:00-5:00	8	EM/C-I LAB-G2(SK)	MOOC(VA)
Tuesday	9:00-10:00	1	A&D.E(SK)	MPW-G1(JS)
	10:00-11:00	2	EM/C-I(SK)	MPW-G2(VA)
	11:00-12:00	3	EMI LAB -G1(RS)	EVT(JS)
	12:00-1:00	4	EM/C-I LAB-G2(SK)	IECD(VA)
	1:00-2:00	5	LUNCH	
	2:00-3:00	6	EED-G1 (VA)	PS LAB(RS)G1
	3:00-4:00	7	EED-G2 (SK)	EVT LAB(JS) (G2)
	4:00-5:00	8		PS(RS)
Wednesday	9:00-10:00	1	A&D.E LAB-G1 (SK)	PS LAB(RS)G1
	10:00-11:00	2	IT-I-G2 (JS)	IECD LAB (VA) G2
	11:00-12:00	3	A&D.E(SK)	IECD(VA)
	12:00-1:00	4	EEM (VA)	SPI&M(SK)
	1:00-2:00	5	LUNCH	
	2:00-3:00	6		SPI&M(SK)
	3:00-4:00	7	SCA(RS)	SCA(JS)
	4:00-5:00	8		
Thursday	9:00-10:00	1	MOOC(RS)	IT-II (SK) (G1)
	10:00-11:00	2	EMI(RS)	IECD LAB (VA) G2
	11:00-12:00	3	EM/C-I LAB-G1(SK)	MOOC(VA)
	12:00-1:00	4	EMI LAB -G2(RS)	EVT(JS)
	1:00-2:00	5	LUNCH	
	2:00-3:00	6		PS(RS)
	3:00-4:00	7	EED-G1 (VA)	EVT LAB(JS) (G1)
	4:00-5:00	8	EED-G2 (SK)	PS LAB(RS)G2
Friday	9:00-10:00	1	MOOC(RS)	IECD LAB (VA) G1
	10:00-11:00	2	EMI(RS)	IT-II (SK) (G2)
	11:00-12:00	3	IT-I-G1(RS)	MPW-G1(JS)
	12:00-1:00	4	A&D.E LAB-G2 (SK)	MPW-G2(VA)
	1:00-2:00	5	LUNCH	
	2:00-3:00	6	EM/C-I LAB-G1(SK)	MPW-G1(JS)
	3:00-4:00	7	EMI LAB -G2(RS)	MPW-G2(VA)
	4:00-5:00	8	EEM (VA)	SPI&M(SK)

Note: 1. Sh. Jagdeep (JS) (HOD) 4. Sh. Sandeep Kumar (SK) (GF1)
2. Ms. Varsha Aggarwal (VA) (Lecturer) 5. Sh. Kuldeep Mittal (KM) (GF2)
3. Sh. Rajesh Saini (RS) (Lecturer)

HOD *[Signature]*
Electrical Engg.
Date: 28/07/26

Endst. No:

A copy of above is forwarded for information to the following by E-mail

1. PA to Principal for information
2. All concerned staff members
3. HOD Applied Science
4. O/I Website of Institute
5. Student's whatsapp group
6. O/I Time Table of Electrical Engg. Department

HOD
Electrical Engg.

